

FOR IMMEDIATE RELEASE

**iCODING ANNOUNCES CHARTER MEMBERSHIP IN COMMON
LICENSE CONSORTIUM FOR FPGA-BASED INTELLECTUAL
PROPERTY**

In an industry first, IP (Intellectual Property) developers team with Xilinx to make IP cores available thru a single set of licensing terms, speeding development time for FPGA developers

San Diego, CA-Sept. 13, 2001-iCODING, a leader in the development of Turbo Coding forward error correction (FEC) cores, today joined the Common License Consortium, founded by Xilinx and members of the AllianceCORE program. The puzzle of putting multiple IP's together just got easier through the organization of the Common License Consortium.

The Consortium has established a set of licensing terms, the SignONCE IP License, with the goal of easing the acquisition of FPGA-based IP cores. The terms of the agreement are applicable across all Consortium members. Pricing and technical requirements can be negotiated on a member-by-member basis without the need for further legal assistance.

According to Brian Edmonston, President of iCODING, "Negotiations for licensing can be one of the most time consuming pieces of the IP transfer process. The SignONCE IP License will reduce the time it takes for this process by months, benefiting everyone involved."

Mr. Edmonston further states: "The SignONCE IP License program is a natural for customers of programmable logic IP. Now not only do customers have a large selection of IP cores to choose from, it has also become easier for them to attain licenses for these cores. We've always emphasized that FPGA's put the solution in customers' hands more quickly than ASIC's."

The Common License Consortium represents an IP industry first, bringing customers access to IP cores from various members. The availability of cores is expected to grow as consortium membership grows, thus further allowing customers to easily assemble all of the IP puzzle pieces they need in order to complete their products.

About the Common License Consortium

Xilinx and members of its AllianceCORE third-party IP provider program have joined together to form the Common License Consortium. All consortium members have agreed to accept a single set of common IP licensing terms, called the SignONCE IP License, for acquiring FPGA-based IP cores. The result is a first for the IP industry - a single set of licensing terms, giving customers access to IP cores from multiple vendors for use in Xilinx programmable logic. For more information on this program, go to www.xilinx.com/ipcenter/signonce.htm.

About iCODING

iCODING produces high speed high performance FEC cores, founded in 1999 and is headquartered in San Diego, California. As a leader in Turbo Coding Technology iCODING is an active participant in the IEEE 802.16 standard. iCODING currently offers its S2000 DVB-RCS compliant Turbo Decoder core through the Xilinx

AllianceCORE Program. The S2000 Turbo Code is based on convolutional constituent codes and is designed for Quasi-Error-Free (QEF) performance. For more information on iCODING and Turbo Codes, visit their website at <http://www.icoding.com>.

Contact:

Brian Edmonston

858-487-5595

<mailto:brian@icoding.com>